

Roll Number
 In Figures: _____
 In Words: _____

PR XI (01) 16
COMPUTER SCIENCE
 Inter Part – I (New)
 (Fresh)
 Fic. No. _____
 (For Board's Office use only)

Superintendent
 Signature / Stamp:

COMPUTER SCIENCE
 Inter Part – I (New)
 (Fresh)

Fic. No. _____
 (For Board's Office use only)

Marks: 75

Time Allowed: 3 Hours

Note: There are THREE sections in this paper i.e. Section A, B and C.

Attempt Section-A on the same paper and return it to the Superintendent within the given time.

No marks will be awarded for Cutting, Erasing or Overwriting. Marks of Identification will lead to UFM case, Mobile Phone etc are not allowed in the examination hall.

Time Allowed: 20 minutes

Section – A

Marks: 15

Q-I Write the correct option i.e. A, B, C or D in the empty box provided opposite to each part.

- | | | | | | | |
|-------|--|------------------------------|-------------------------------|----------------------------------|------------------|--------------------------|
| i. | The distance between the pixels on the monitor is called..... | A. Size | B. Resolution | C. Dot Pitch | D. Refresh Rate | <input type="checkbox"/> |
| ii. |is not a portable computer. | A. Laptop | B. PDA | C. Notebook | D. Mainframe | <input type="checkbox"/> |
| iii. |is the fastest memory. | A. RAM | B. ROM | C. Cache memory | D. PROM | <input type="checkbox"/> |
| iv. |is volatile memory. | A. RAM | B. ROM | C. PROM | D. EEPROM | <input type="checkbox"/> |
| v. | System bus connects the Central Processing Unit to.....on the motherboard. | A. Register | B. Main Memory | C. ALU | D. Input Unit | <input type="checkbox"/> |
| vi. |is small memory device available in the CPU to store data temporarily. | A. CU | B. Register | C. ALU | D. Memory | <input type="checkbox"/> |
| vii. | An example of output device is | A. Printer | B. Monitor | C. Mouse | D. ROM | <input type="checkbox"/> |
| viii. |is a new technology cable for connecting storage devices to computer. | A. IDE | B. PCI | C. FD | D. SATA | <input type="checkbox"/> |
| ix. | OSI model haslayers. | A. 7 | B. 5 | C. 6 | D. 3 | <input type="checkbox"/> |
| x. | A collection of two or more connected computers to share the resources and data is called..... | A. Router | B. Network | C. Path | D. Medium | <input type="checkbox"/> |
| xi. | The frequency range of radio signal is between 3 KHz to GHz. | A. 1 | B. 2 | C. 3 | D. 4 | <input type="checkbox"/> |
| xii. | The GPS system consists of Satellites. | A. 20 | B. 22 | C. 24 | D. 25 | <input type="checkbox"/> |
| xiii. | A row in a table is also known as..... | A. Column | B. Relation | C. Tuple | D. Field | <input type="checkbox"/> |
| xiv. | PROM stands for | A. Previous read only memory | B. Practical read only memory | C. Programmable read only memory | D. None of these | <input type="checkbox"/> |
| xv. | Which one is the shortcut key of Redo in MS Word? | A. Ctrl + Y | B. Ctrl + Z | C. F12 | D. None of these | <input type="checkbox"/> |

PR XI (01) 16
COMPUTER SCIENCE

Inter Part –I (New)
(Fresh)

Note: Time allowed for Section – B and Section – C is 2 Hours and 40 minutes.

Section – B

Marks: 40

Q-II Answer any TEN parts. Each part carries FOUR marks.

1. What is Processing Operation?
2. Differentiate between Shareware and Freeware.
3. What is the importance of cache memory in a computer?
4. Give few characteristics of secondary storage devices.
5. What is the function of ALU in the computer?
6. Differentiate between CISC and RISC processor architecture.
7. List different parts of motherboard.
8. Why cooling system is important for a computer?
9. Compare TCP/IP Model with OSI Model.
10. Differentiate between short distance and long distance wireless communication.
11. What is Hotspot?
12. Define the term database and DBMS with the help of example.
13. Give any four advantages of database system.

Section – C

Marks: 20

Note : Attempt any TWO questions. All questions carry equal marks.

- Q-III** What are impact and non impact printers? Explain any two types of printers in each category.
- Q-IV** Explain RAM and ROM along with their types in detail.
- Q-V** What is Instruction Cycle? Explain different Phases of CPU instruction cycle.